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Patent Amendment

Amendments to the Specification:

Replace the first paragraph on page 1 with the following:

This application claims priority to European Application Serial No. 00402948.4, filed October 24, 2000 (TI-31362EU). US Patent Application Serial No. [[\_\_\_\_\_]] 6,751,706 (TI-31366US) is incorporated herein by reference.

Please replace the paragraph starting on page 5, line 8 with:

Figures 3A and 3B illustrate examples ~~Figure 3 illustrates an example~~ showing the advantages of energy management for a multiprocessor system;

Please replace the paragraph starting on page 17, line 15 with:

Figure ~~11~~ 12 illustrates an embodiment of the invention that accurately measures energy information regarding the operation of a specific task. By increasing the accuracy of the energy information associated with a task, the probability of success for a proposed global scenario is similarly increased.

Please replace the paragraph starting on page 17, line 19 with:

Figure ~~11~~ 12 provides a more detailed block diagram of MPU 12. An MPU core 130 includes a TaskID register 132 and a compare circuit 134. Core 130 is coupled to instruction cache 20a and data cache 20b. Counters 78 monitor activity within the core. Counters 78 have enable ports (En) coupled to the output of compare circuit 134. Each processor that may independently execute a task (i.e., "autonomous" processors such as an MPU or DSP, but generally not a co-processor or a DMA physical channel) may have

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a TaskID register 132 and a compare circuit 134. Thus, the device shown in Figure 9 might have three TaskID registers 132 corresponding to the MPU 12, DSP1 14a and DSP2 14b.